

Notice of Allowability

Application No.

10/711,306

Examiner

LAM T. MAI

Applicant(s)

BUER, KENNETH V.

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 1/26/2006.
2. ☒ The allowed claim(s) is/are 2-6.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some* c) ☐ None of the:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|---|--|
| 1. <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 6. <input checked="" type="checkbox"/> Interview Summary (PTO-413),
Paper No./Mail Date _____ |
| 3. <input type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____ | 7. <input checked="" type="checkbox"/> Examiner's Amendment/Comment |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit
of Biological Material | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance |
| | 9. <input type="checkbox"/> Other _____ |

DETAILED ACTION

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with John Platt (602)382-6367 on 4/5/2006.

The application has been amended as follows:

Claims 1 and 7-10 are cancelled.

Claims 2-5 are amended as following:

Claim 2. A MMIC having transistors, a RF input, a RF output and a RF matching structural, wherein said MMIC is configured to receive DC bias feed, from off the MMIC, directly at the RF matching structure; wherein said RF input is in electrical communication with said transistors, wherein said transistor are in electrical communication with RF matching structure and wherein RF matching structure is in electrical communication with said RF output.

Claim 3. A MMIC having transistors, a RF input, a RF output and a RF matching structural, wherein transistors are configured to receive one of plurality of DC bias feed, and wherein at least one of the plurality of DC bias feeds is not received to the MMIC at the edge of the MMIC; wherein said RF input is in electrical communication with said transistors, wherein said transistor are in electrical communication with RF

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matching structure and wherein RF matching structure is in electrical communication with said RF output.

Claim 4. A MMIC having transistors, a RF input, a RF output and a RF matching structural, wherein said RF matching structure is prepared with an ohmic material to receive said bias feed directly from off the MMIC; wherein said RF input is in electrical communication with said transistors, wherein said transistor are in electrical communication with RF matching structure and wherein RF matching structure is in electrical communication with said RF output.

Claim 5. A MMIC having transistors, a RF input, a RF output and a RF matching structural, wherein said transistor and RF matching structure lie in a plane, wherein said RF matching structure is prepared with material to receive said bias feed from a direction out of said plane of the MMIC chip; wherein said RF input is in electrical communication with said transistors, wherein said transistor are in electrical communication with RF matching structure and wherein RF matching structure is in electrical communication with said RF output.

Specification

The lengthy specification has not been checked to the extent necessary to determine the presence of all possible minor errors. Applicant's cooperation is requested in correcting any errors of which applicant may become aware in the specification.

Allowable Subject Matter

Claims 2-6 are allowable.

The following is an examiner's statement of reasons for allowance: Claim 2 is allowed over the prior art of record. The prior art of record, considered individually or in combination fails to fairly teach or suggest claimed structure comprising, among other limitations, a novel and unobvious limitations of "wherein said MMIC is configured to receive DC bias feed, from off the MMIC, directly at the RF matching structure" structurally and functionally interconnected with other limitations in the manner as cited in the claim.

Claim 3 is allowed over the prior art of record. The prior art of record, considered individually or in combination fails to fairly teach or suggest claimed structure comprising, among other limitations, a novel and unobvious limitations of "wherein at least one of the plurality of DC bias feeds is not received to the MMIC at the edge of the MMIC" structurally and functionally interconnected with other limitations in the manner as cited in the claim.

Claim 4 is allowed over the prior art of record. The prior art of record, considered individually or in combination fails to fairly teach or suggest claimed structure comprising, among other limitations, a novel and unobvious limitations of "wherein said RF matching structure is prepared with an ohmic material to receive said bias feed directly from off the MMIC" structurally and functionally interconnected with other limitations in the manner as cited in the claim.

Claim 5 is allowed over the prior art of record. The prior art of record, considered individually or in combination fails to fairly teach or suggest claimed structure comprising, among other limitations, a novel and unobvious limitations of "wherein said

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RF matching structure is prepare with a material to receive said bias feed from a direction out of said plane of the MMIC chip" structurally and functionally interconnected with other limitations in the manner as cited in the claim and dependent claim 6.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Cited References

The prior art made of record and not replied upon is considered pertinent to application's disclosure. The cited references relate to dc bias feeding to MMIC subject matter.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to LAM T. MAI whose telephone number is (571)272-1807. The examiner can normally be reached on 5:30 am - 4:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Barnie Rexford can be reached on (571) 272-7492. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Lam T Mai
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